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In the Claims:

Please amend the claims as indicated hereafter.

1. (original) A method of defective pixel address detection for an image sensor, comprising:

(a) comparing a defective pixel address with a sensor address, wherein the defective pixel address is stored in a memory element in advance when the image sensor is tested;

(b) outputting a defective pixel flag if the sensor address is equal to the defective pixel address:

(c) increasing an index value by one unit and returning to the step (a);

(d) comparing the defective pixel address with an empty signature if the sensor address is not equal to the defective pixel address;

(e) increasing the index value by one unit if the defective pixel address is equal to the empty signature, and returning to the step (a); and

(f) returning to the step (a) if the defective pixel address is not equal to the empty signature.

2. (original) The method of claim 1, wherein the defective pixel address stored in the memory element is in an ascending order.

3. (original) The method of claim 1, wherein the memory element is a fuse array.

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4. (original) The method of claim 1, wherein the defective pixel address stored in the memory element is in a format consisting of a row address and a column address of the image sensor.

5. (original) A method of defective pixel address detection for an image sensor, comprising the steps of:

storing a plurality of defective pixel addresses during the image sensor is tested reading a pixel address of the image sensor;

fetching one of the defective pixel addresses;

comparing the first fetched defective pixel address with the pixel address of the sensor address;

outputting a defective pixel flag if the pixel address is equal to the defective pixel address;

increasing an index value by one unit and fetching another defective pixel address which is indexed next to the first fetched defective pixel address;

comparing the defective pixel address with an empty signature if the pxiel address is not equal to the defective pixel address;

increasing the index value by one unit if the defective pixel address is the empty signature, and then fetching another defective pixel address which is indexed next to the first fetched defective pixel address; and



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fetching another defective pixel address which is indexed next to the first fetched defective pixel address if the defective pixel address is not the empty signature.

6. (original) The method of claim 5, wherein the plurality of the defective pixel addresses are stored in a memory element.

7. (original) The method of claim 6, wherein the defective pixel addresses stored in the memory element is in as ascending order.

8. (original) The method of claim 6, wherein the defective pixel address stored in the memory element is in a format consisting of a row address and a column address of the image sensor.

9. (original) The method of claim 6, wherein the memory element is a fuse array.

10. (currently amended) A device of defective pixel address detection for an image sensor, comprising:

a memory element, for storing a plurality of defective pixel addresses—during—the image sensor is tested, wherein the defective pixel addresses are arranged in an ascending order in the memory element; and

an address comparator, coupling to the memory element, for receiving a pixel address of the image sensor and one of the defective pixel addresses, and then comparing the pixel address

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of the image sensor and the defective pixel addresses, wherein the address comparator outputs a defective pixel flag if the pixel address is equal to the defective pixel address.

11. (original) The device of claim 10, wherein the memory element is a fuse array.

12. (original) The device of claim 10, wherein the memory element further comprises an index for indicating a location of one of the defective pixel addresses.

Claim 13. (canceled)